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PATENT

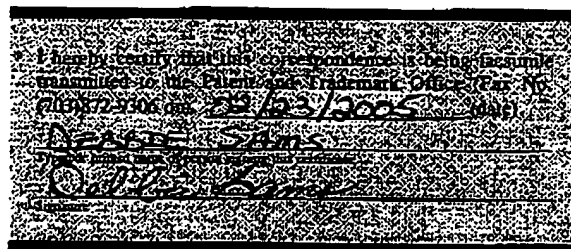
IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

OK
FOR
ENTRY
DE
3/05

Applicant: Rajasekhar Pullela, *et al.*
Serial No.: 10/033,525
Filed: December 28, 2001
Title: TRANS-ADMITTANCE TRANS-IMPEDANCE
LOGIC FOR INTEGRATED CIRCUITS
Grp./A.U.: 2816
Examiner: Dinh Thanh Le

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

AMENDMENT UNDER 37 C.F.R. § 1.116

The Applicants have carefully considered this application in connection with the Examiner's Final Action mailed December 23, 2004, and respectfully request reconsideration of this application in view of the following amendment and remarks.